

## Mechanical Data

Item	Dimension	Unit
Module dimension	33.59 × 23.62 × 1.65	mm
View area	25.49 × 17.65	mm
Active area	23.49 × 15.65	mm
Dot Size	0.215 × 0.215	mm
Dot Pitch	0.245 × 0.245	mm

## Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	V <sub>DD</sub>	-0.3	4	V	1, 2
Supply Voltage for I/O Pins	V <sub>DDIO</sub>	-0.3	V <sub>DD</sub> +0.5	V	1, 2
Supply Voltage for Display	V <sub>CC</sub>	0	15	V	1, 2

## Electronical Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	V <sub>DD</sub>	—	2.8	3	3.5	V
Supply Voltage for Display	V <sub>CC</sub>	—	11	13	15	V
High Level Input	V <sub>IH</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0.8×V <sub>DD</sub>	—	V <sub>DD</sub>	V
Low Level Input	V <sub>IL</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0	—	0.2×V <sub>DD</sub>	V
High Level Output	V <sub>OH</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0.9×V <sub>DD</sub>	—	V <sub>DD</sub>	V
Low Level Output	V <sub>OL</sub>	I <sub>OUT</sub> = 100μA, 3.3MHz	0	—	0.1×V <sub>DD</sub>	V
Operating Current for VDD	I <sub>DD</sub>	Note 4	—	250	400	μA
		Note 5	—	250	400	μA
Operating Current for VCC	I <sub>CC</sub>	Note 4	—	35	40	mA
		Note 5	—	45	50	mA
Sleep Mode Current for VDD	I <sub>DD, SLEEP</sub>	—	—	1	10	μA
Sleep Mode Current for VCC	I <sub>CC, SLEEP</sub>	—	—	1	10	μA

## Feature

1. 96x64 dots
2. Built-in Controller SSD1305T7R1
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 6800, 8080, SPI, I2C
6. Sunlight readable & polarizer optional

Pin NO.	Symbol	Description			
1	NC	No connection			
2	VCC	Power supply for analog circuit			
3	VCOMH	Com Voltage Output.			
4	IREF	Reference current input pin			
5~12	D7~D0	Data bus			
13	E/RD#	Data read			
14	R/W#	Data write			
15	D/C#	Data/ Command control			
16	RES#	Reset signal input			
17	CS#	Chip select input.			
18	FR	Pin outputs RAM write synchronization signal			
19	BS2	Communicating Protocol Select			
20	BS1	68XX-parallel	80XX-parallel	Serial	
		BS1	0	1	0
		BS2	1	1	0
21	VDDIO	Power supply for interface logic level			
22	VDD	Power supply for logic circuit			
23	VCIR	Reserved pin			
24	BGGND	This pin must be connected to ground.			
25	VBREF	This is a reserved pin. It should be kept NC			
26	NC	No connection			
27	FB	This is a reserved pin. It should be kept NC			
28	VDDB	This is a reserved pin. It must be connected to VDD.			
29	GDR	This is a reserved pin. It should be kept NC			
30	VSS	Ground.			
31	NC	No connection			

OLED Graphic type

## RET00964A OLED Graphic 96x64 dots

### Dimension drawing

